

Message from the Technical Program Chairs

iSES 2020

It is with distinct pleasure that we welcome you to the 6th IEEE International Symposium on Smart Electronic Systems (IEEE-iSES) to be virtually held in Chennai, India from 14–16th December 2020. Over the last few years, iSES has evolved into a leading research forum for the academic and industrial community to share their innovative ideas and research results. The primary objective of IEEE-iSES is to provide a platform for both hardware and software researchers to interact under one umbrella for further development of smart electronic systems.

This year, iSES consists of six tracks: Nanoelectronic VLSI and Sensor Systems (NVS), Energy-Efficient, Reliable VLSI Systems (ERS), Hardware/Software for Internet of Things and Consumer Electronics (IoT), Hardware/Software for Secure Information Processing (SIP), Hardware/Software for AI, Robotics, and Automation (AIR), and Hardware/Software for Vehicular Intelligent Systems (VIS). iSES 2020 attracted high quality papers from across the globe. After a thorough review process, papers were selected for presentation as 39 Regular Papers, 36 Short Papers, 7 Student Research Forum Papers, and 3 Research Demo Papers. The accepted papers also include the Regular and Short Papers in the Special Sessions on (i) Hardware Accelerators for AI and Machine Learning, and (ii) Integrated Circuits, Systems for Emerging Electronic Systems. Regular papers are of 6 pages, Short Papers are of 4 pages, Student Research Forum Papers are of 4 pages and Research Demo Papers are 2 pages in the proceedings. The reviews were conducted by a strong technical program committee and reviewers who are among the leading researchers in their fields. The iSES 2020 proceedings will be published and indexed at IEEE Xplore. The papers from previous iterations of this conference have been extended and published in top archival journals in the past. iSES 2020 is a truly global conference, with participation of authors and program committee members from countries such as USA, India, Canada, Australia, Norway, Italy, France, Greece, UK, Japan and China.

We would like to express our sincere gratitude to the track chairs for their tireless efforts in managing the tracks by forming a strong program committee, assigning reviews, consolidating the scores, and finally making the recommendations. We would like to thank the technical program committee members for their time and efforts in providing detailed and timely reviews. We would also like to thank all of the authors without whose contributions, the technical program would not have been possible. We would like to thank the Steering Committee and the Organizing Committee for their support and cooperation in every step of the process. We hope you have a very productive iSES 2020 and wish you a very pleasant virtual conference.



Yasuhiro Takahashi
Gifu University, Japan
iSES 2020 Program Chair
yasut@gifu-u.ac.jp



Tosiron Adegbiya
University of Arizona, USA
iSES 2020 Program Vice-Chair
tosiron@arizona.edu



Linga Reddy Cenkeramaddi
University of Agder, Norway
iSES 2020 Program Vice-Chair
linga.cenkeramaddi@uia.no